

PATENT NUMBER

<p>Q.I.P.E.</p> <p>SCANNED <u>Cx</u> Q.A. <u>ne</u></p>	<p>PATENT DATE</p>
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APPLICANTS

TITLE

Methodology for the design of high-performance communication architectures for system-on-chips using communication architecture tuners

PTO-2040
12/99[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) _____ (Date)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____	_____ (Primary Examiner) _____ (Date)			ISSUE FEE	
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